

ABSTRACT OF THE DISCLOSURE

It is intended to provide a semiconductor integrated circuit device and adjustment method of the same semiconductor integrated
5 circuit device, capable of adjusting an analog signal outputted
from an incorporated analog signal generating section without
outputting it outside as an analog value. An analog signal AOUT
is outputted from an analog signal generating section 3 in which
an adjustment signal AD is inputted. The analog signal AOUT is
10 inputted to a judgment section 1, in which it is compared and
judged with a predetermined value and then a judgment signal JG
is outputted. The judgment signal JG acts on a predetermined
signal storing section 4 as an internal signal and the adjustment
signal AD is fetched into the predetermined signal storing
15 section 4. Further, the judgment signal JG is outputted as
digital signal through an external terminal T2 and an external
tester device acquires the adjustment signal and stores the
acquired adjustment signal in the predetermined signal storing
section 4. Consequently, the analog signal can be adjusted as
20 analog value without being outputted outside and an adjustment
test can be carried out with a simple tester device and according
to a simple test method accurately and rapidly.